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APPLICANTS

Kazunari Kurita, Tokyo, JAPAN;
 Shinsuke Sadamitsu, Tokyo, JAPAN;
 Hiroyuki Takao, Tokyo, JAPAN;
 Masataka Hourai, Tokyo, JAPAN;

** CONTINUING DATA *****

This application is a 371 of PCT/JP04/11050 08/02/2004

** FOREIGN APPLICATIONS *****

JAPAN 2003-360251 10/21/2003

** IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

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| Foreign Priority claimed | <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No | STATE OR COUNTRY | SHEETS DRAWINGS | TOTAL CLAIMS | INDEPENDENT CLAIMS |
|--------------------------------|---|--|-----------------|--------------|--------------------|
| 35 USC 119(a-d) conditions met | <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No | <input type="checkbox"/> Met after Allowance | | | |
| Verified and Acknowledged | /MARISSA W CHAET/ Examiner's Signature | Initials | JAPAN | 5 | 8 |

ADDRESS

CLARK & BRODY
 1090 VERMONT AVENUE, NW
 SUITE 250
 WASHINGTON, DC 20005
 UNITED STATES

TITLE

Process for producing high-resistance silicon wafers and process for producing epitaxial wafers and soi wafers (as amended)

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|--------------------------------|---|--|
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